

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.:

Patent No.:

Filed:

Issue Date:

Title:

Commissioner for Patents
Washington, D.C. 20231

POWER OF ATTORNEY BY ASSIGNEE OF ENTIRE INTEREST
(REVOCATION OF PRIOR POWERS)

As assignee of record of each of the patents listed in the table of attachment A,

REVOCATION OF PRIOR POWERS OF ATTORNEY

all powers of attorney previously given in each of the listed patents are hereby revoked, and

NEW POWER OF ATTORNEY

the following attorneys/agents are hereby appointed to prosecute this patent/application and to transact all business in the Patent and Trademark Office connected therewith: I hereby appoint all attorneys of Thomas, Kayden, Horstemeyer & Risley, LLP, who are listed under the USPTO Customer Number shown below as the attorneys to prosecute this patent/application and to transact all business in the United States Patent and Trademark Office connected therewith, recognizing that the specific attorneys listed under that Customer Number may be changed from time to time at the sole discretion of Thomas, Kayden, Horstemeyer & Risley, LLP, and request that all correspondence about the application be addressed to the address filed under the same USPTO Customer Number:

15650

Patent Trademark Office

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ASSIGNEE OF ENTIRE INTEREST

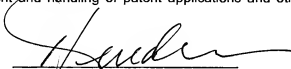
BROADCOM CORPORATION
5300 California Avenue
Irvine, California 92617-3038

ASSIGNEE CERTIFICATION

The certification under 37 C.F.R. §3.73(b) establishing the right of assignee to take action is attached hereto along with documentation evidencing same. Further, in my official position with Broadcom Corporation, I am authorized to sign documents and otherwise act on its behalf in connection with the management and handling of patent applications and other intellectual property matters.

Date: _____

9/12/11



Dee Henderson
Director - Intellectual Property Administration
Broadcom Corporation

Attachment A

| No. | Patent No | BD No. / TKHR No. | Patent Title | Issue Date | BD Assign't (Reel/Frame) |
|-----|-----------|---------------------------|---|------------|--------------------------|
| 1 | 8000653 | BU4149C1 050228-3930 | METHOD AND SYSTEM FOR SELECTIVE EQUALIZATION ENABLEMENT BASED ON MODULATION TYPE | 08/16/11 | 016291/0941 |
| 2 | 7925213 | BU4825 050228-3940 | METHOD AND SYSTEM FOR AUDIO SIGNAL PROCESSING FOR BLUETOOTH WIRELESS HEADSETS USING A HARDWARE ACCELERATOR | 04/12/2011 | 017369/0364 |
| 3 | 7590432 | BU4837.1 050228-3950 | MOBILE COMMUNICATION DEVICE WITH LOW POWER RECEIVER FOR SIGNAL DETECTION | 09/15/09 | 017231/0722 |
| 4 | 7639309 | BU4884 050228-3960 | METHOD AND SYSTEM FOR ANALOG VIDEO NOISE REDUCTION BY BLENDING FIR AND IIR FILTERING | 12/29/09 | 017246/0809 |
| 5 | 7932955 | BU4889 050228-3970 | METHOD AND SYSTEM FOR CONTENT ADAPTIVE ANALOG VIDEO NOISE DETECTION | 04/26/11 | 017255/0860 |
| 6 | 7689819 | BU4901 050228-3980 | METHOD AND SYSTEM FOR A SELF-BOOTING ETHERNET CONTROLLER | 03/30/10 | 017135/0978 |
| 7 | 7523299 | BU4901.1 050228-3990 | METHOD AND SYSTEM FOR MODIFYING OPERATION OF ROM BASED BOOT CODE OF A NETWORK ADAPTER CHIP | 04/21/09 | 017150/0696 |
| 8 | 7590396 | BU4924 050228-4010 | MULTIMODE COMMUNICATION DEVICE WITH SHARED SIGNAL PATH PROGRAMMABLE FILTER | 09/15/09 | 017231/0703 |
| 9 | 7343541 | BU5047 050228-4020 | DATA INTEGRITY IN PROTOCOL OFFLOADING | 03/11/08 | 017780/0016 |
| 10 | 6798420 | BU1233.7 050228-4030 | VIDEO AND GRAPHICS SYSTEM WITH A SINGLE-PORT RAM | 09/28/04 | 011607/0583 |
| 11 | 7209992 | BU1266C2D1 050228-4040 | GRAPHICS DISPLAY SYSTEM WITH UNIFIED MEMORY ARCHITECTURE | 04/24/07 | 010689/0710 |
| 12 | 7515935 | BU4807.1 050228-4050 | METHOD AND SYSTEM FOR FLEXIBLE FM TUNING | 04/07/09 | 017177/0919 |
| 13 | 7706836 | BU4807.3 050228-4060 | METHOD AND SYSTEM FOR A RADIO DATA SYSTEM (RDS) DEMODULATOR FOR A SINGLE CHIP INTEGRATED BLUETOOTH AND FREQUENCY MODULATION (FM) TRANSCEIVER AND BASEBAND PROCESSOR | 04/27/10 | 017169/0846 |
| 14 | 7650527 | BU5106 050228-4070 | MIPS RECOVERY TECHNIQUE | 01/19/10 | 017404/0856 |
| 15 | 7746408 | BU5116 050228-4080 | METHOD AND SYSTEM FOR IMPROVING THE APPEARANCE OF DEINTERLACED CHROMA FORMATTED VIDEO | 06/29/10 | 017343/0689 |
| 16 | 7844222 | BU5186 050228-4090 | METHOD AND SYSTEM FOR CHANGING PRIORITY OF SLAVE FRAMES IN MULTIWIRED COEXISTENCE | 11/30/10 | 018497/0894 |
| 17 | 7738708 | BU5253.1 050228-4100 | SYSTEM AND METHOD FOR ALIGNING CHROMA PIXELS | 06/15/10 | 018753/0174 |
| 18 | 7881385 | BU2007D1 050228-4110 | Video decoding system supporting multiple standards | 02/01/11 | 012761/0381 |

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|------------|------------------|------------------------------|---|-------------------|-------------------------------------|
| 19 | 7826535 | BU2008 050228-4120 | ADAPTIVE PIXEL PROCESSING | 11/02/10 | 017837/0913 |
| 20 | 7096245 | BU2017 050228-4130 | INVERSE DISCRETE COSINE TRANSFORM SUPPORTING MULTIPLE DECODING PROCESSES | 08/22/06 | 012761/0426 |
| 21 | 6925174 | BU2041 050228-4140 | INTERACTION BETWEEN ECHO CANCELLER AND PACKET VOICE PROCESSING | 08/02/05 | 012768/0955 |
| 22 | 7920697 | BU2041C1 050228-4150 | INTERACTION BETWEEN ECHO CANCELLER AND PACKET VOICE PROCESSING | 04/05/11 | 012768/0955 |
| 23 | 6985853 | BU2046 050228-4160 | COMPRESSED AUDIO STREAM DATA DECODER MEMORY SHARING TECHNIQUES | 01/10/06 | 012672/0184 |
| 24 | 7162416 | BU2046C1 050228-4170 | COMPRESSED AUDIO STREAM DATA DECODER MEMORY SHARING TECHNIQUES | 01/09/07 | 012672/0184 |
| 25 | 7653070 | BU4614 050228-5660 | METHOD AND SYSTEM FOR SUPPORTING EFFICIENT AND CACHE-FRIENDLY TCP SESSION LOOKUP OPERATIONS BASED ON CANONICALIZATION TAGS | 01/26/10 | 016912/0361 |